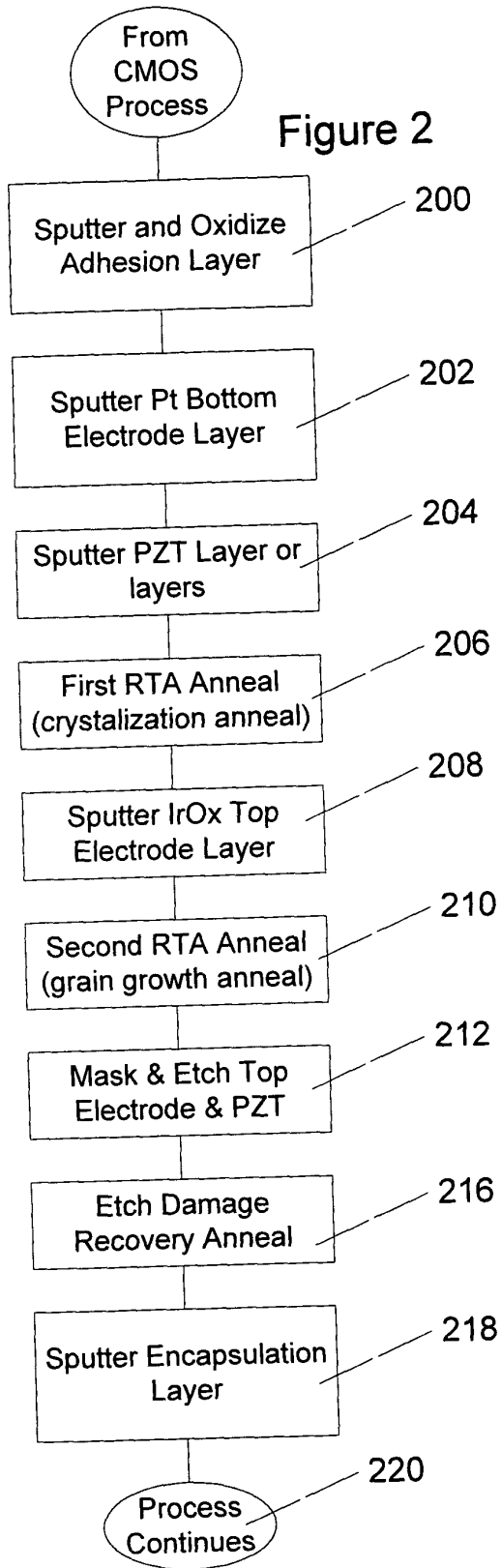


Figure 1

Figure 2



From CMOS Process

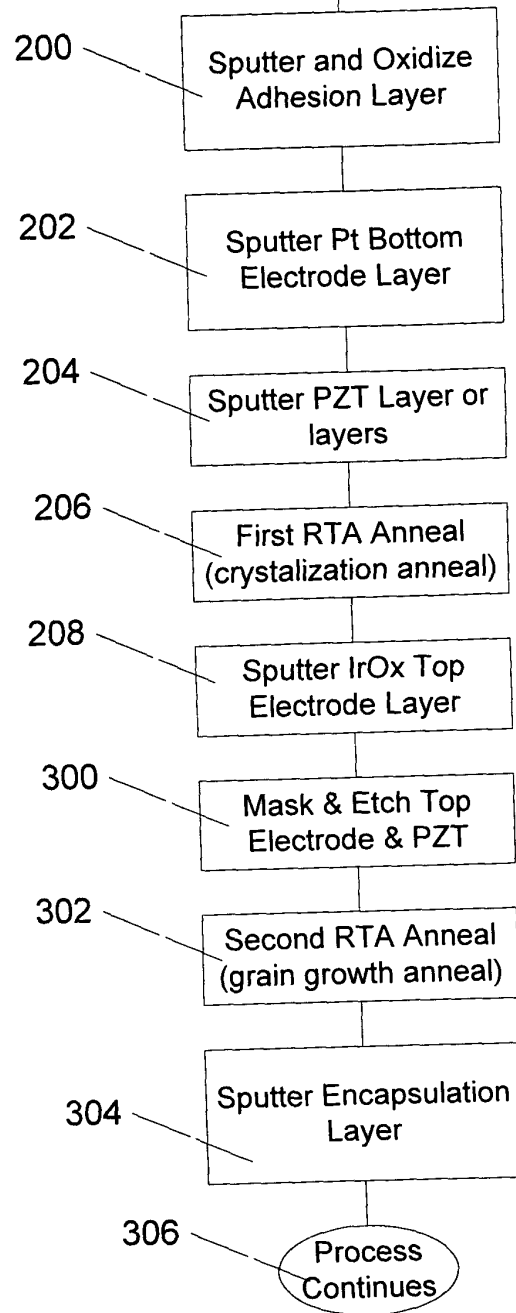


Figure 3

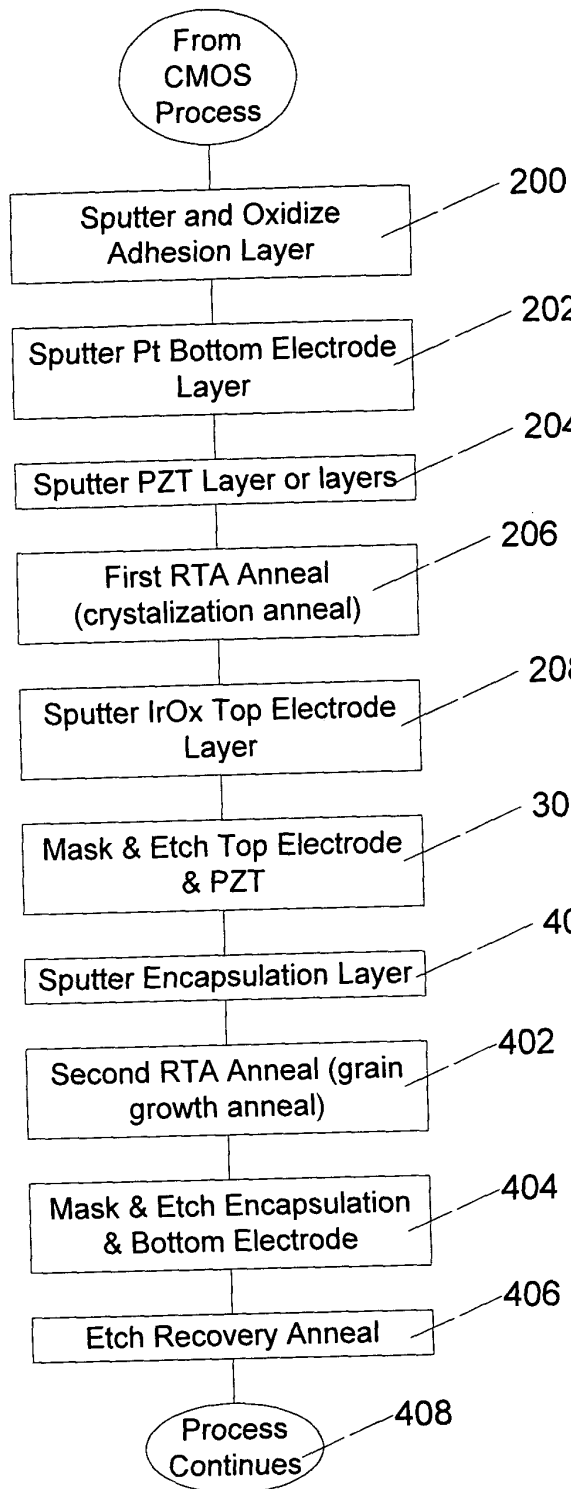


Figure 4

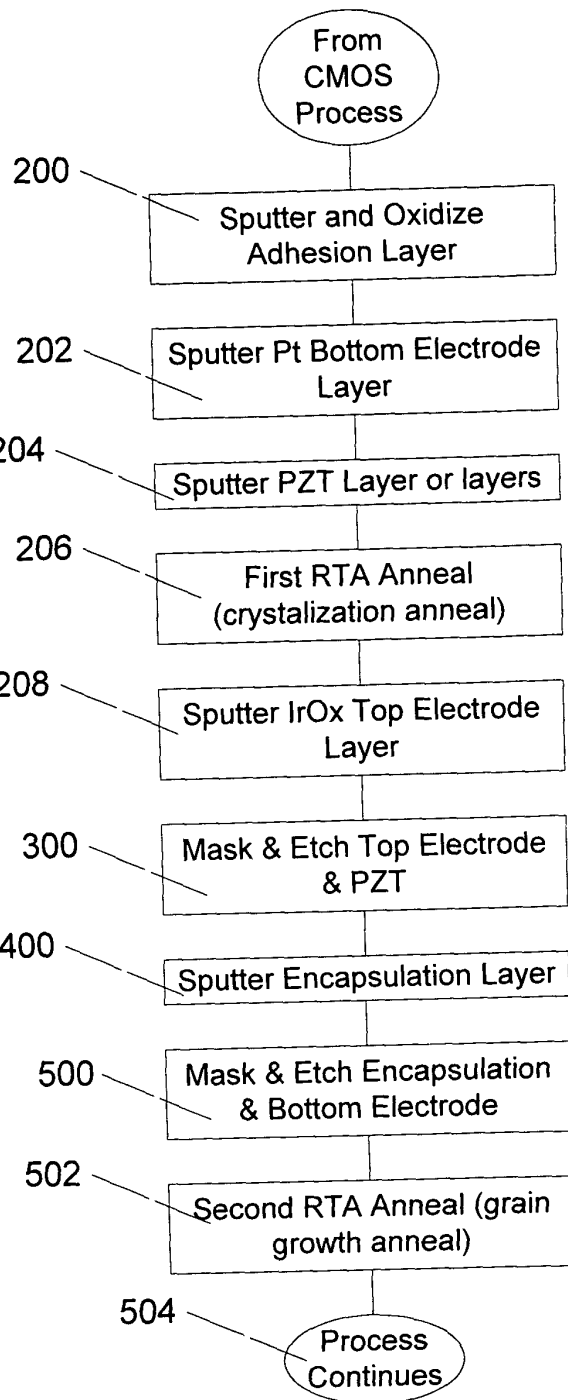


Figure 5